

Applicant: Sung-We Cho                      Serial No.            not yet assigned  
Filing Date: September 29, 2003              Group:                not yet assigned  
Title:                      MTCMOS FLIP-FLOP CIRCUIT CAPABLE OF RETAINING DATA IN SLEEP MODE

INFORMATION DISCLOSURE CITATION  
FORM PTO-1449 (Modified)

U.S. PATENT DOCUMENTS

Exam <u>Init</u>	<u>Ref</u>	Document <u>Number</u>	Issue <u>Date</u>	<u>Name</u>	<u>Class</u>	Sub <u>Class</u>
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FOREIGN PATENT DOCUMENTS

Exam <u>Init</u>	<u>Ref</u>	Document <u>Number</u>	Publication <u>Date</u>	<u>Country</u>	<u>Name</u>
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OTHER DOCUMENTS

Exam <u>Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>
_____	_____	IEEE JOURNAL OF SOLID STATE CIRCUIT, Vol.32, No.6, Satoshi Shigematsu, 1997, entitled A 1-V High-Speed MTCMOS Circuit Scheme for Power-Down Application Circuits.

Examiner: \_\_\_\_\_

Date Considered: \_\_\_\_\_